LOGIC CIRCUIT DEVICES

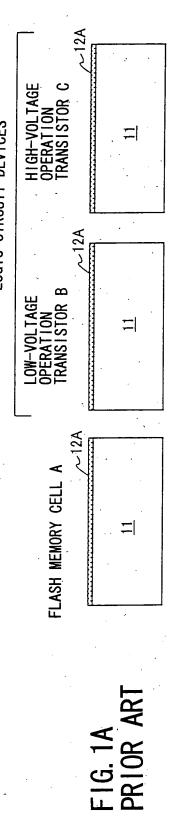
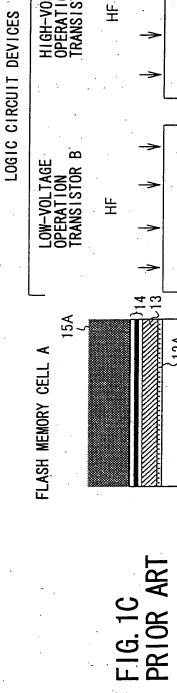


FIG. 1B PRIOR ART



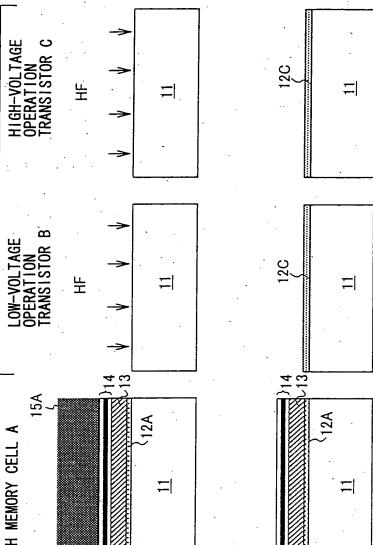
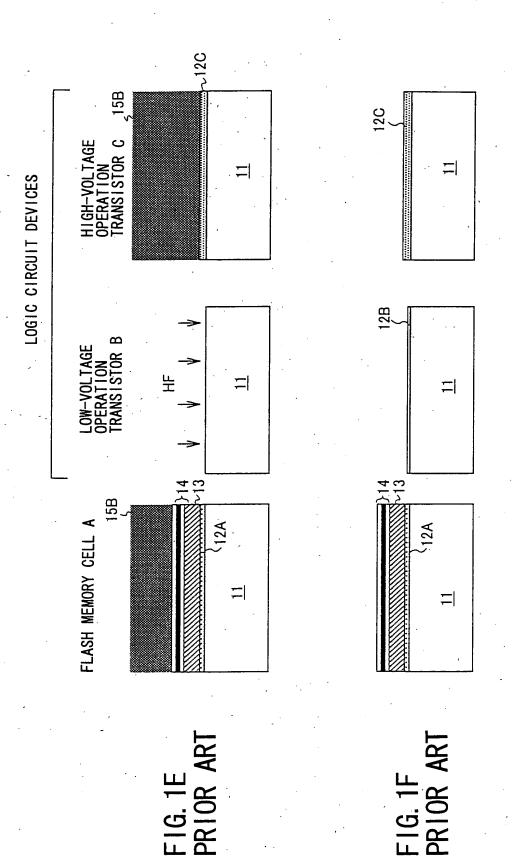
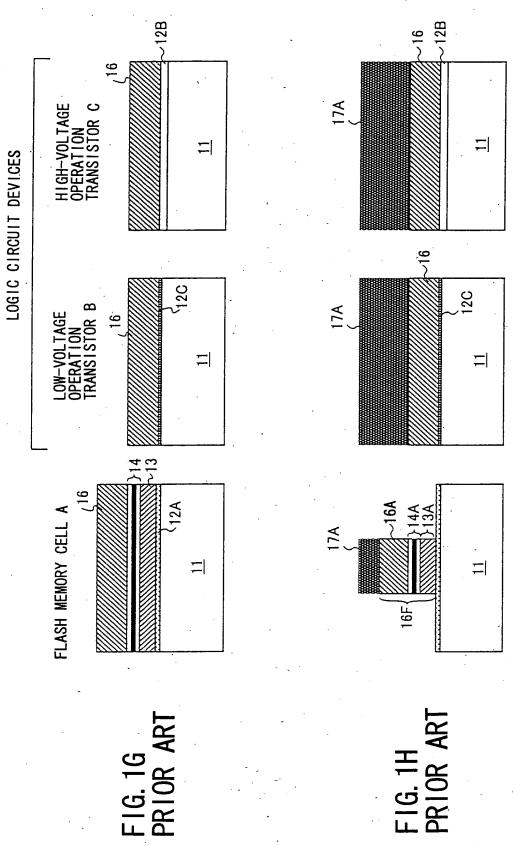
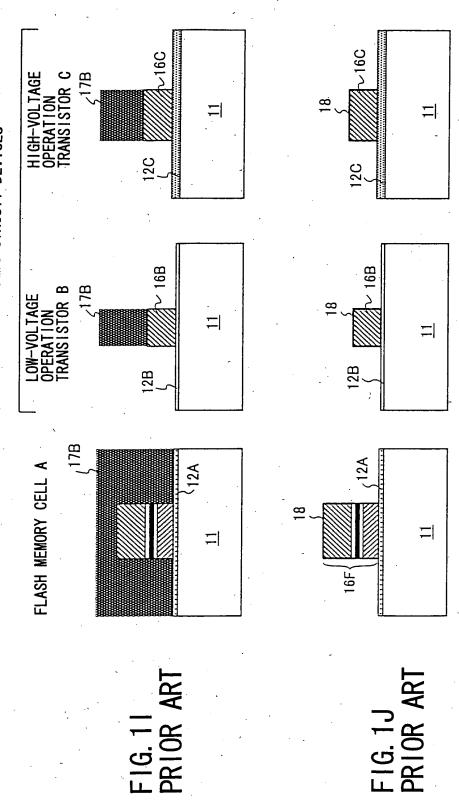


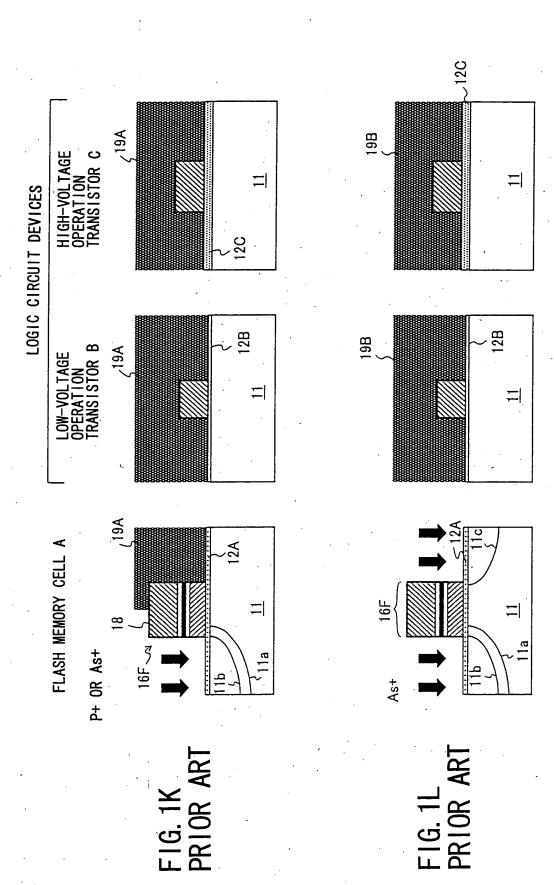
FIG. 1D PRIOR ART

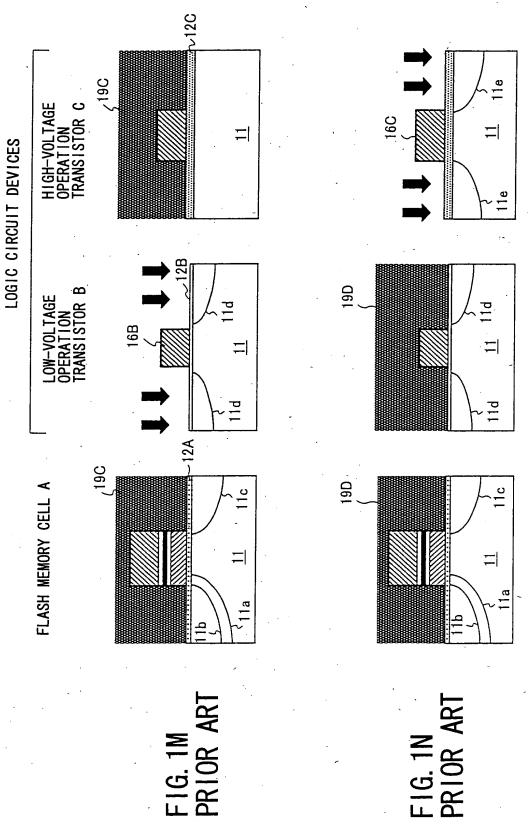




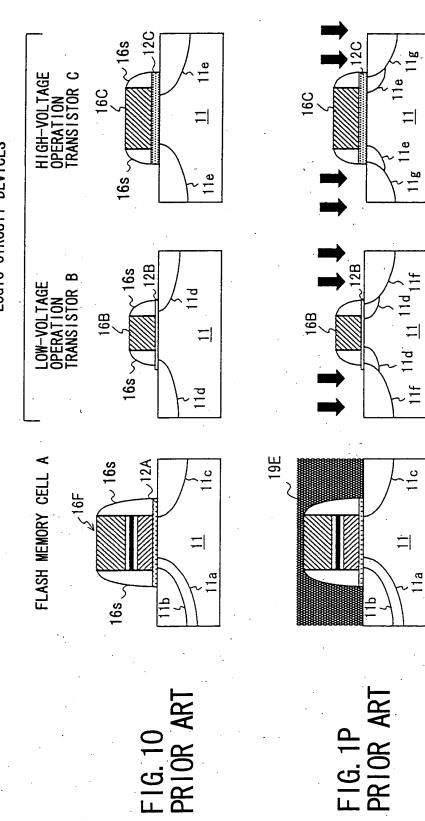
LOGIC CIRCUIT DEVICES







LOGIC CIRCUIT DEVICES



20C 16C 16s HIGH-VOLTAGE OPERATION TRANSISTOR C 16s 20C 20B LOW-VOLTAGE OPERATION TRANSISTOR B **16s** 20B 750 FLASH MEMORY CELL A 20A 16s 12A 16F 20A

LOGIC CIRCUIT DEVICES



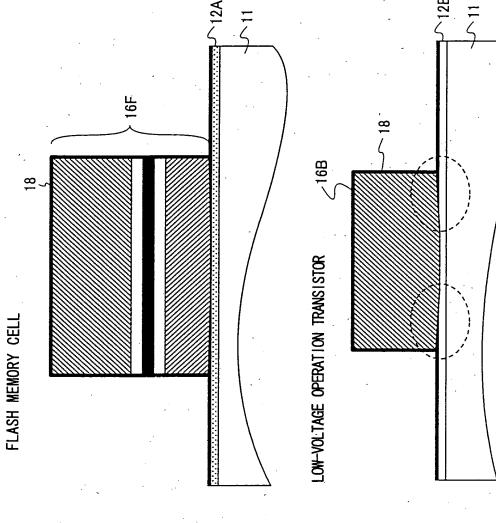


FIG. 2B PRIOR ART

FIG. 3A PRIOR ART

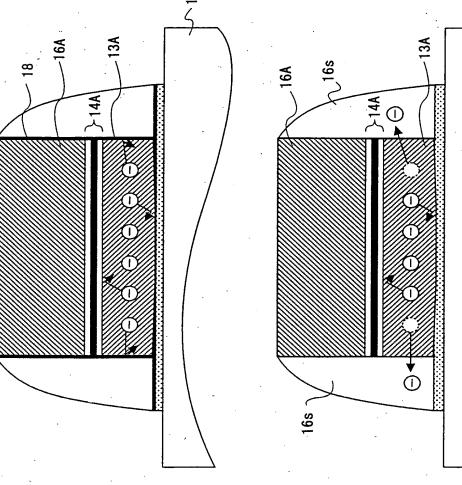
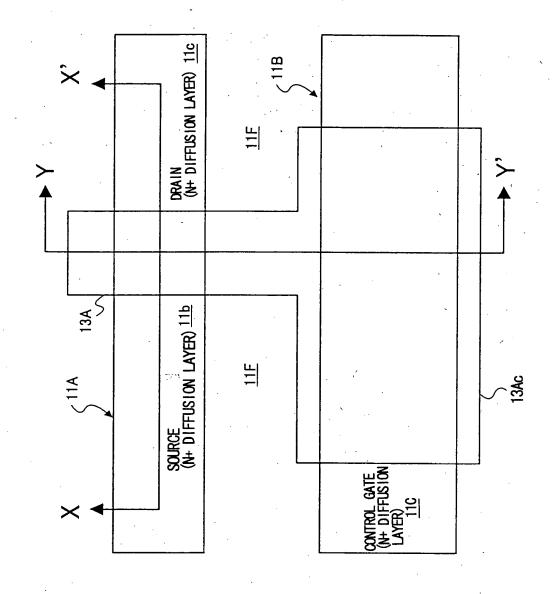
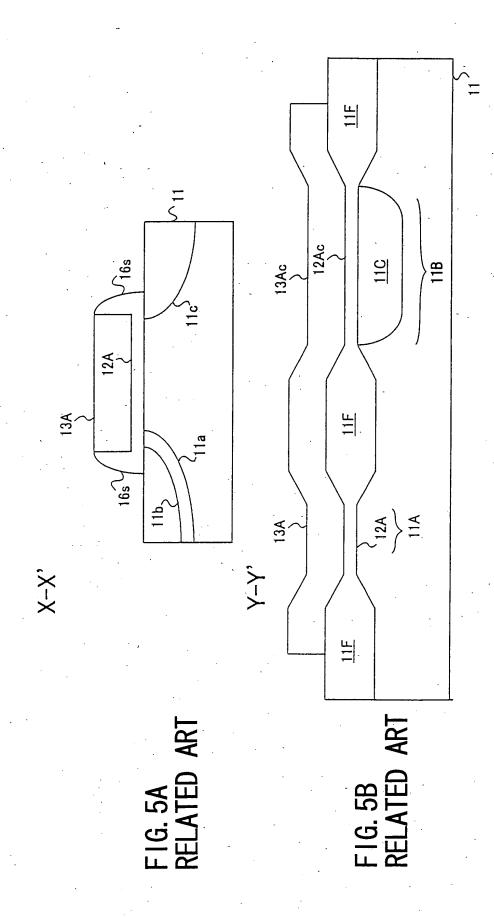
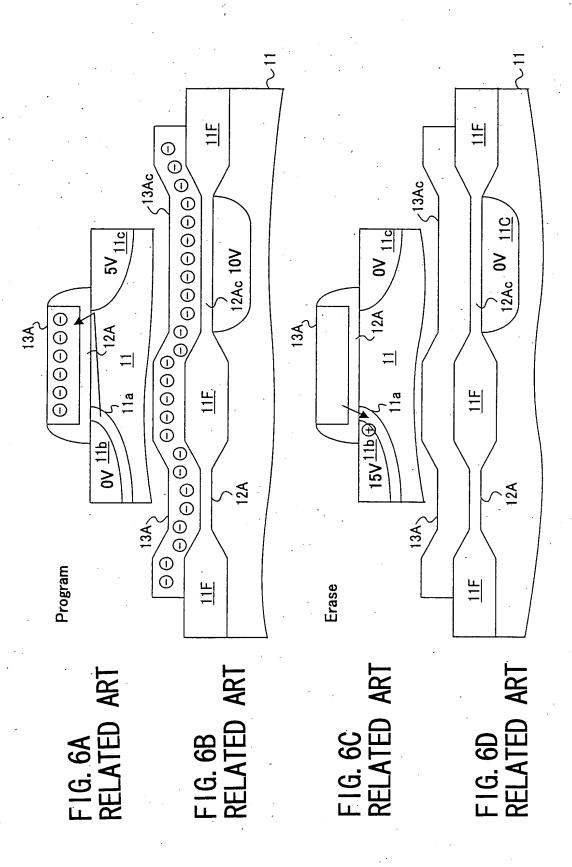


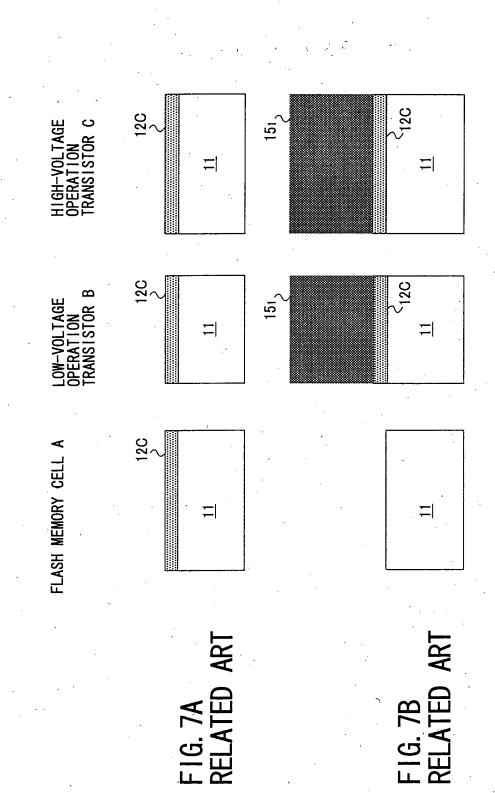
FIG. 3B PRIOR ART

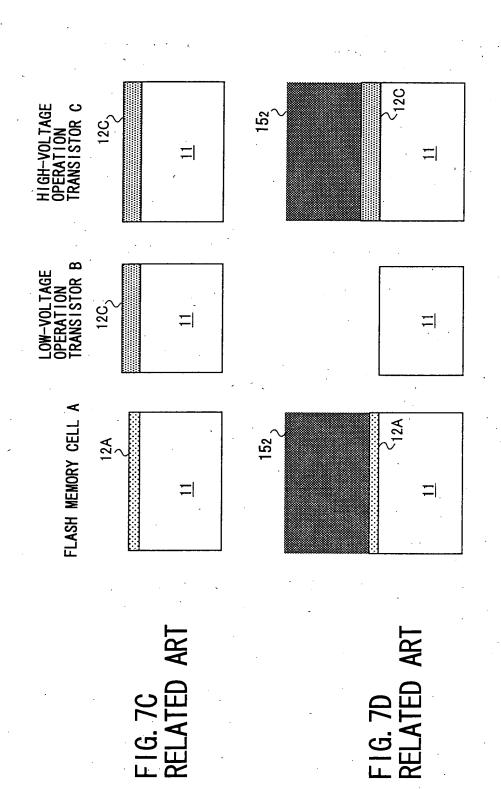
FIG. 4 RELATED ART

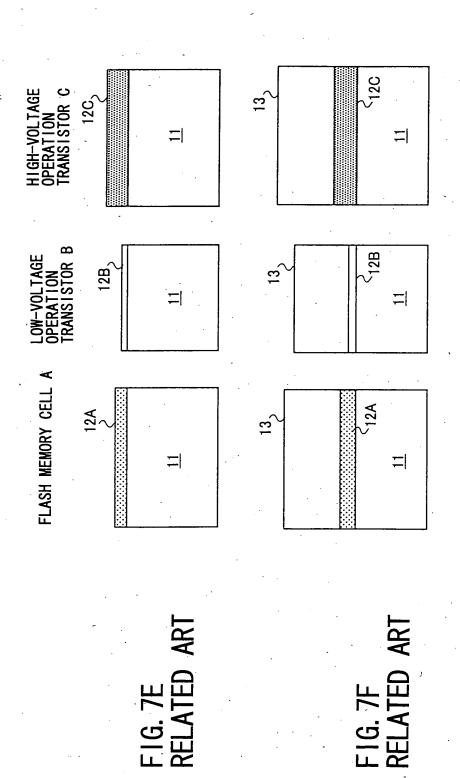












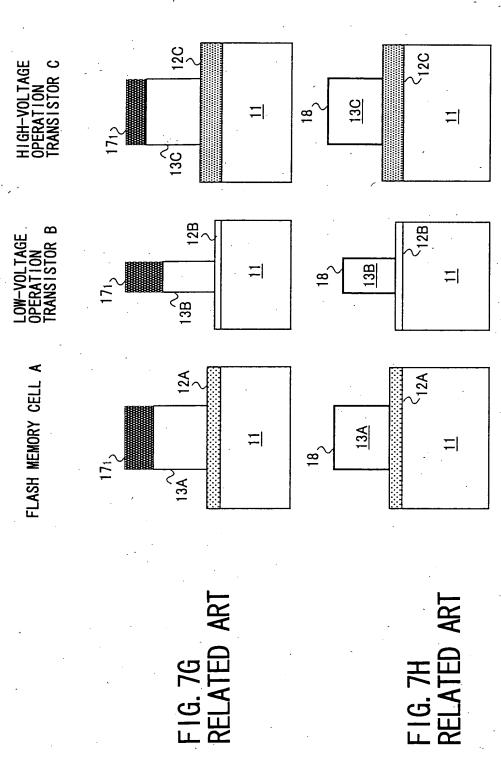
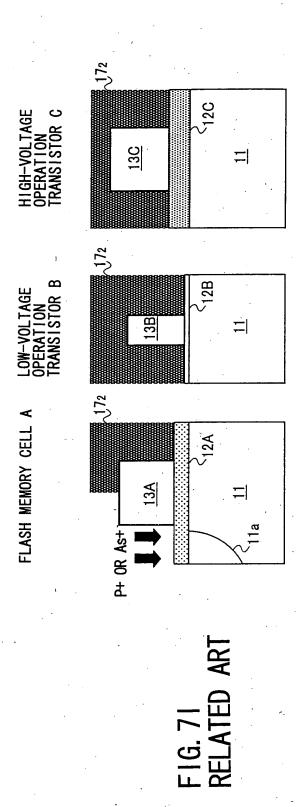


FIG. 7H RELATED ART



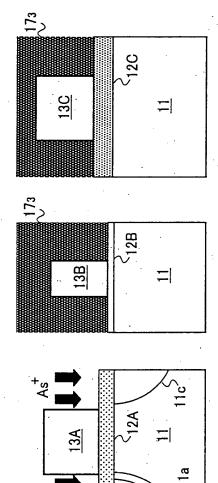


FIG. 7J RELATED ART

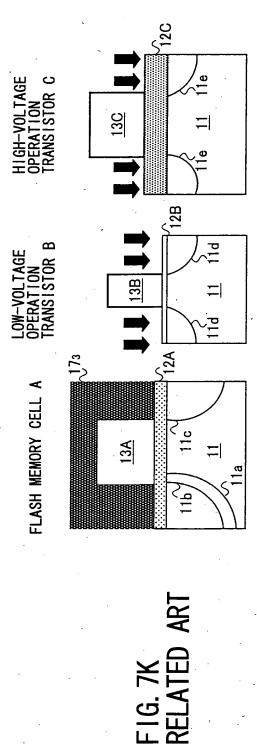
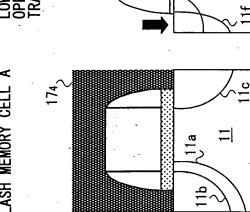
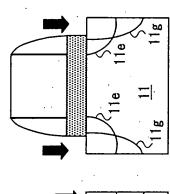


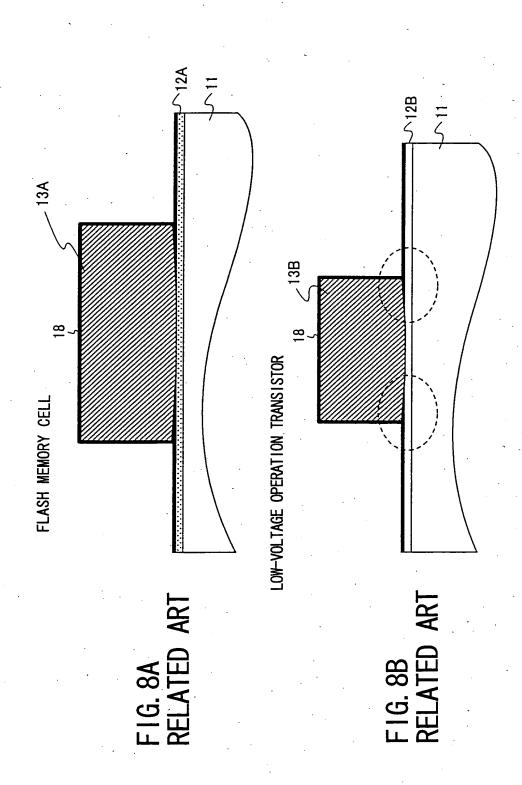
FIG. 7L RELATED ART

FIG. 7M RELATED ART









LOGIC CIRCUIT DEVICES =FLASH MEMORY CELL A ⁽12A

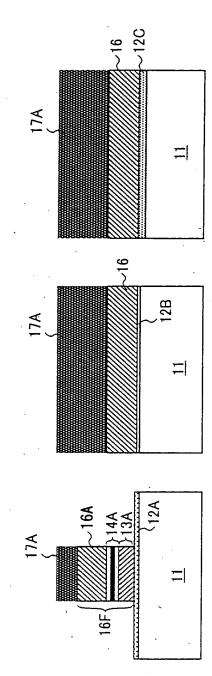
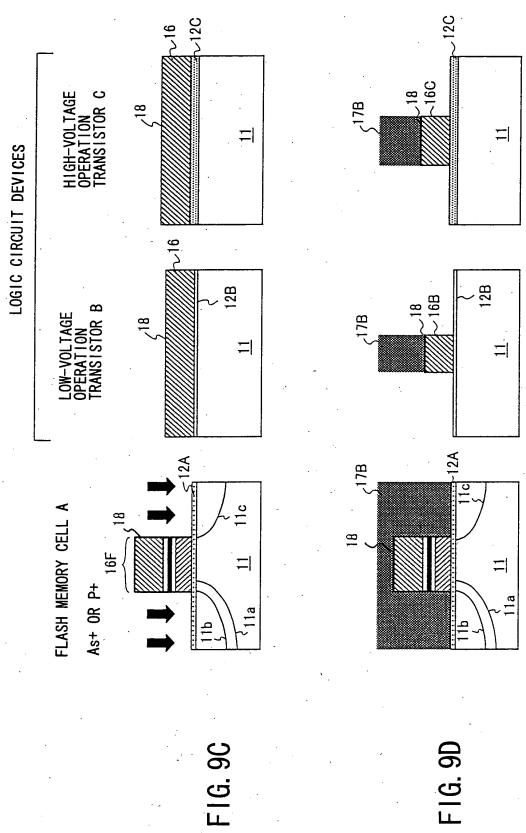
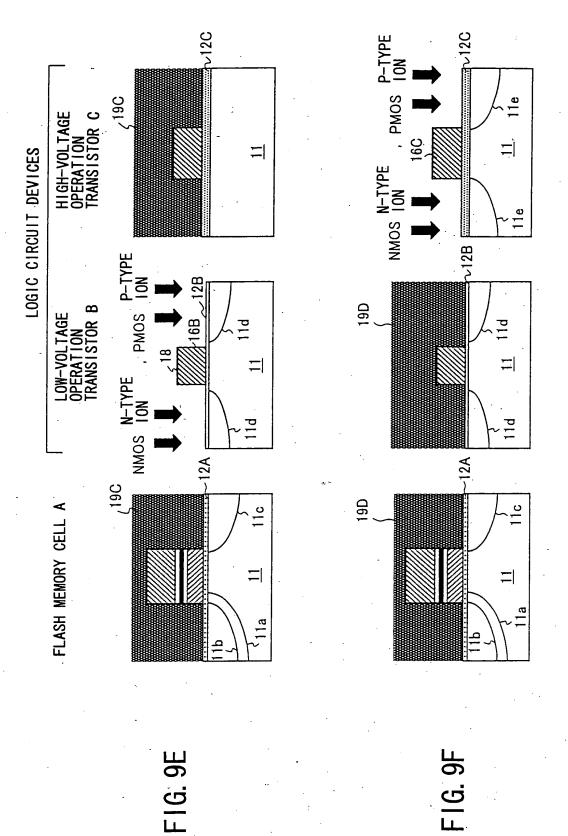


FIG. 9A

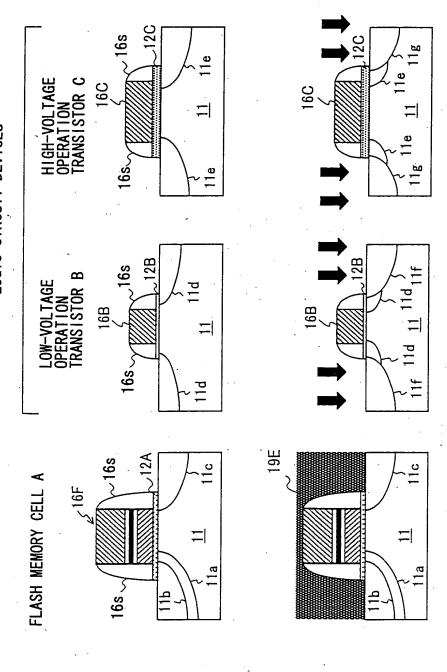
F1G. 9B



F1G. 9D



LOGIC CIRCUIT DEVICES



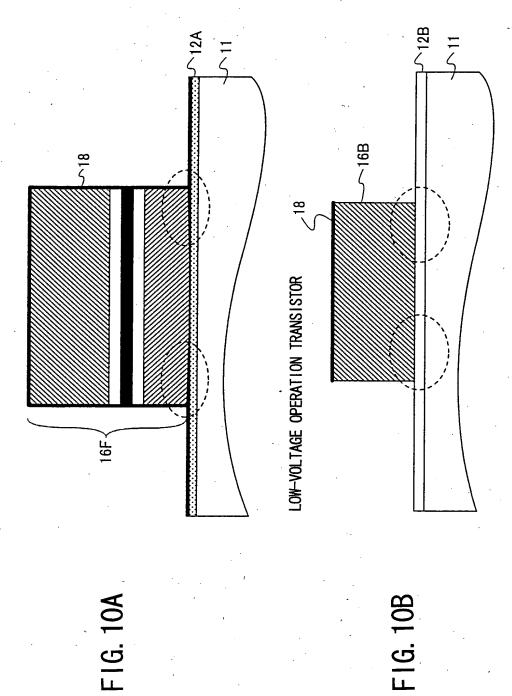
F1G. 9G

F1G. 9H

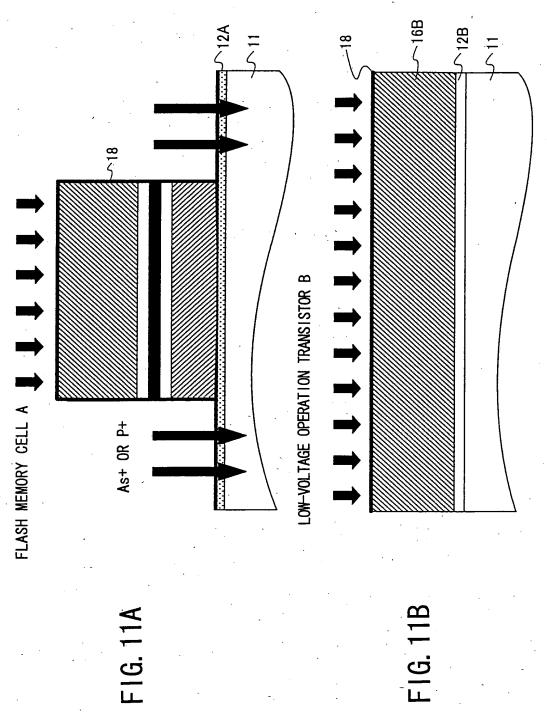
20C HIGH-VOLTAGE OPERATION TRANSISTOR C 16s LOGIC CIRCUIT DEVICES 20C 20B LOW-VOLTAGE OPERATION TRANSISTOR B 16B 20B 20A FLASH MEMORY CELL A 16s 16F 20À

F16, 91

FLASH MEMORY CELL



F1G. 10B



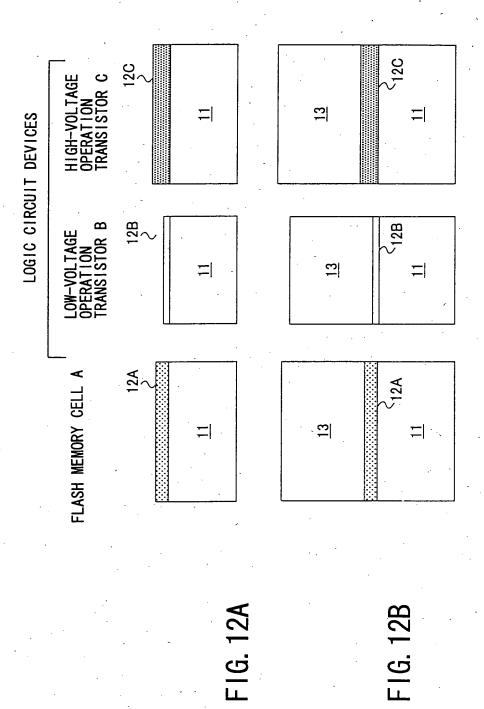
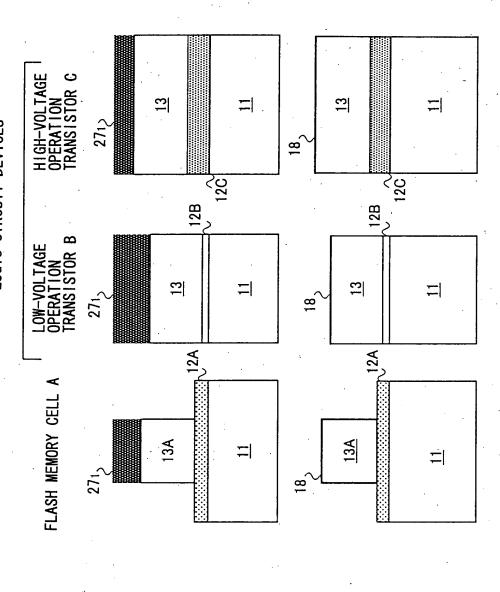
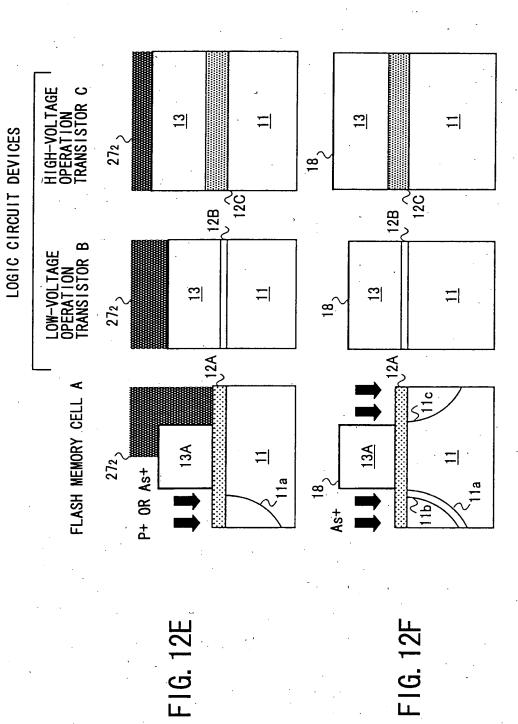


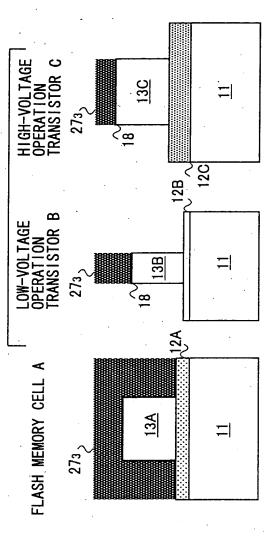
FIG. 12B

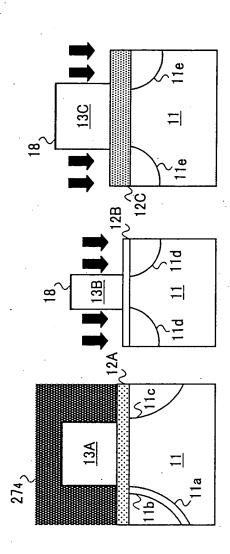
LOGIC CIRCUIT DEVICES



F1G. 12C







F1G. 12G

F1G. 12H

LOGIC CIRCUIT DEVICES

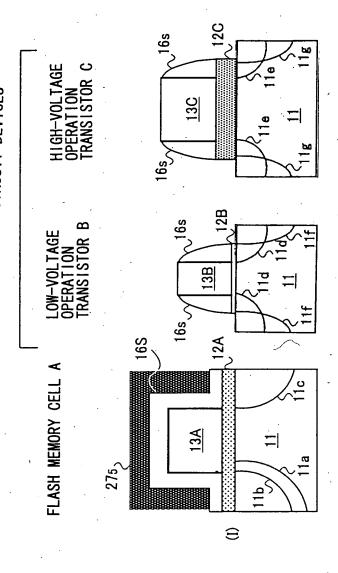
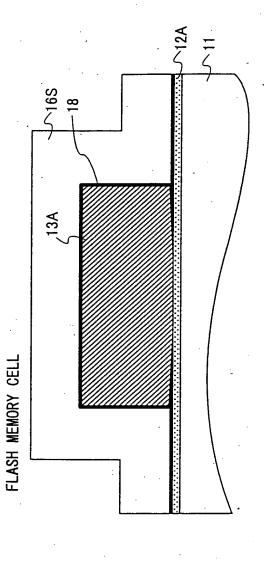


FIG. 121

F1G. 13A



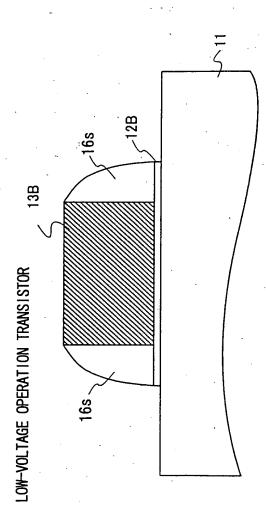


FIG. 13B